

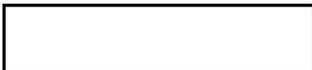


NPIC/TDS-180/67  
11 October 1967

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MEMORANDUM FOR: Imagery Analysis Staff

ATTENTION



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SUBJECT : Coordination of Chip Comparator System

REFERENCE : IAS/TA-DRAFT, Same Subject, Dated 15 September 1967

1. We also view the Chip Comparator coordination problem as one of a sub-system to the overall exploitation problem. To be more specific, while the Chip Comparator as a system must interface directly with the IIS and the Chip Concept (including printers and processors), it is also forerunner of future developments in Image Analysis (improved chip Quality), Materials Handling, Information Handling, and the High Precision Stereo Comparator. However, our current problem remains to be the operational potential, assuming [redacted] will function as well as [redacted] has for the past seven years and that the funding problems with [redacted] can be resolved. The most serious problem facing the users (IAS, PAG and TID) is the lack of available software and computer time.

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2. We concur with your suggested meeting and the naming of an NPIC project officer with the responsibility and authority to act for NPIC rather than the component to which he is assigned.

3. I was pleased with your system approach, but some additions will be needed in paragraph 4. Specifically:

- a. TDS/DS should be involved for technical assistance and feedback to related current and future developments.
- b. SS/TB should be involved for training activities.
- c. SS/LB should be involved for facilities and logistics interface.

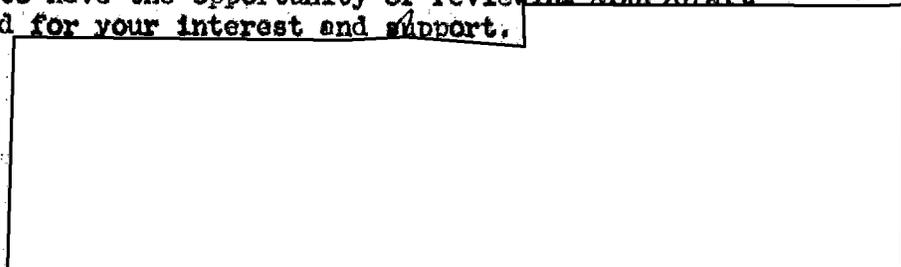
4. I have our Equipment Performance Staff alerted to perform the Acceptance, Service and Operational Suitability Testing on the Comparators.

Excluded from automatic  
downgrading and  
declassification

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SUBJECT: Coordination of Chip Comparator System

5. I was pleased to have the opportunity of reviewing *your draft* memorandum. We are glad for your interest and support.



Assistant for Technical Development, NPIC

Distribution:  
Orig & 1 - Addressee  
2 - NPIC/A/TD

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